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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Alexandre Andreev et. al.

Serial No. :

10/027,642

Filed :

December 20, 2001

For :

METHOD AND APPARATUS TO GENERATE THE FORMAL ANALYSIS OF A MULTIDIRECTIONAL ROUTER

www.pearsoned.com

Group Art Unit : 2812

Examiner :

Atty Docket : / 01-858

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

June 25, 2003

Date _____

Signature

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation
1551 McCarthy Blvd., MS D-106
Milipitas, CA 95035
408-433-7475

Date: June 25, 2003

Respectfully submitted, FAX RECEIVED

JUN 25 2003

TECHNOLOGY CENTER 2800

Leo Peters

Reg. No. 33,562